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74HCT08

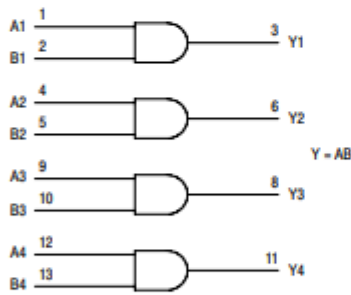
Quad 2-Input AND Gate With LSTTL-Compatible Inputs High-Performance Silicon-Gate CMOS

The 74HCT08 is identical in pinout to the LS08. The device has TTL-compatible inputs.

Features

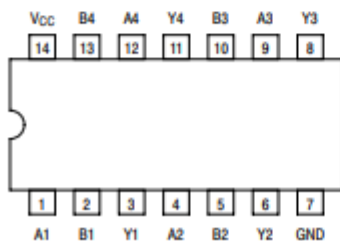
- Output Drive Capability: 10 LSTTL Loads
- TTL/NMOS-Compatible Input Levels
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 4.5 to 5.5 V
- Low Input Current: 1.0 μ A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance With the JEDEC Standard No. 7A Requirements
- ESD Performance: HBM > 2000 V; Machine Model > 200 V
- Chip Complexity: 24 FETs or 6 Equivalent Gates
- These are Pb-Free Devices

LOGIC DIAGRAM



PIN 14 = V_{CC}
 PIN 7 = GND

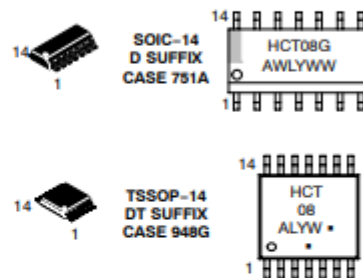
Pinout: 14-Lead Packages (Top View)



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MARKING DIAGRAMS



HCT08 - Device Code
 A - Assembly Location
 WL or L - Wafer Lot
 Y - Year
 WW or W - Work Week
 G or * - Pb-Free Package
 (Note: Microdot may be in either location)

FUNCTION TABLE

Inputs		Output
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.